



CYPRESS

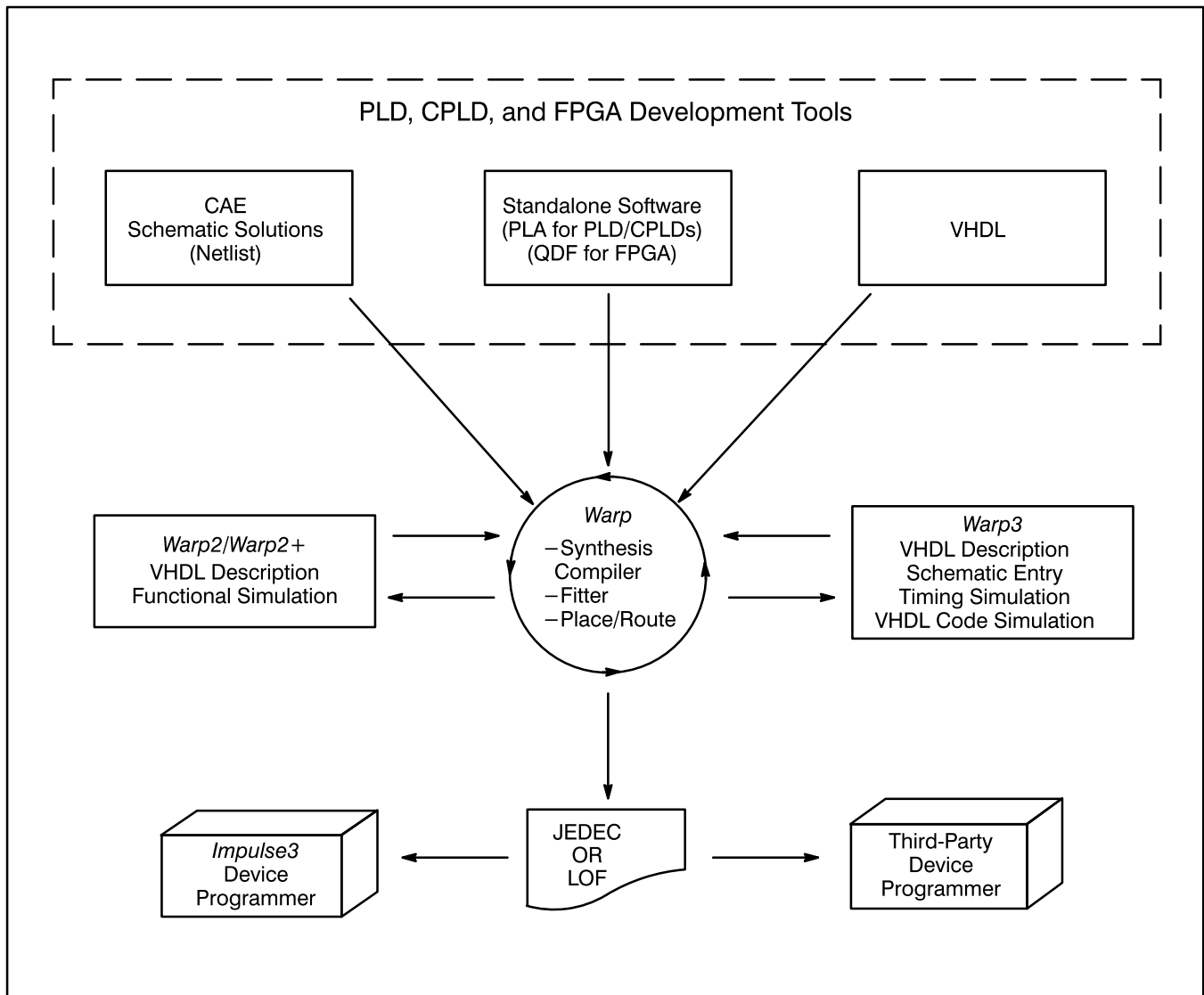
PLD, CPLD, and FPGA Development Tools Overview

A large number of development tools are available for use when designing with Cypress Semiconductor's PLDs, CPLDs, and FPGAs. Many of these tools are available from Cypress, while additional design flow options are available from numerous third-party tool vendors. (For a complete listing of third-party tool vendors, see the Third-Party Tools datasheet.)

Development software is available that provides design entry, synthesis, optimization, fitting, place and route, and simulation. As shown below, this software produces a programming file for use with a device programmer. *Warp2*[™] and *Warp2+*[™] provides

VHDL design description and functional simulation. *Warp3*[™] includes *Warp2+* functionality plus schematic entry, VHDL source code simulation, and timing simulation. In addition, many third-party tools are available and provide various levels of support.

Device programmers use the programming file created by the development tool to program the PLD, CPLD, or FPGA. The *Impulse3*[™] can program any Cypress device and can be upgraded to program other manufacturers' devices. Many third-party programmers are available that can be used to program a wide array of devices including those from Cypress.



Warp2 and *Warp3* are trademarks of Cypress Semiconductor Corporation.

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