



UltraLogic™ 256-Macrocell CPLD

Features

- 256 macrocells in 16 logic blocks
- In-System Reprogrammable (ISR™)
- Fully PCI compliant
- Full JTAG compatibility
- 3.3V or 5V operation
- Programmable speed/power options
- 128 or 160 I/O pins
- 4 dedicated inputs/clocks
- No hidden delays
- High speed
 - $f_{MAX} = 125$ MHz
 - $t_{PD} = 10$ ns
 - $t_s = 5$ ns
 - $t_{CO} = 5.5$ ns
- Available in 160-pin TQFP and 208-pin PQFP packages
- Pin compatible with the Ultra39192 and the Ultra39320

Functional Description

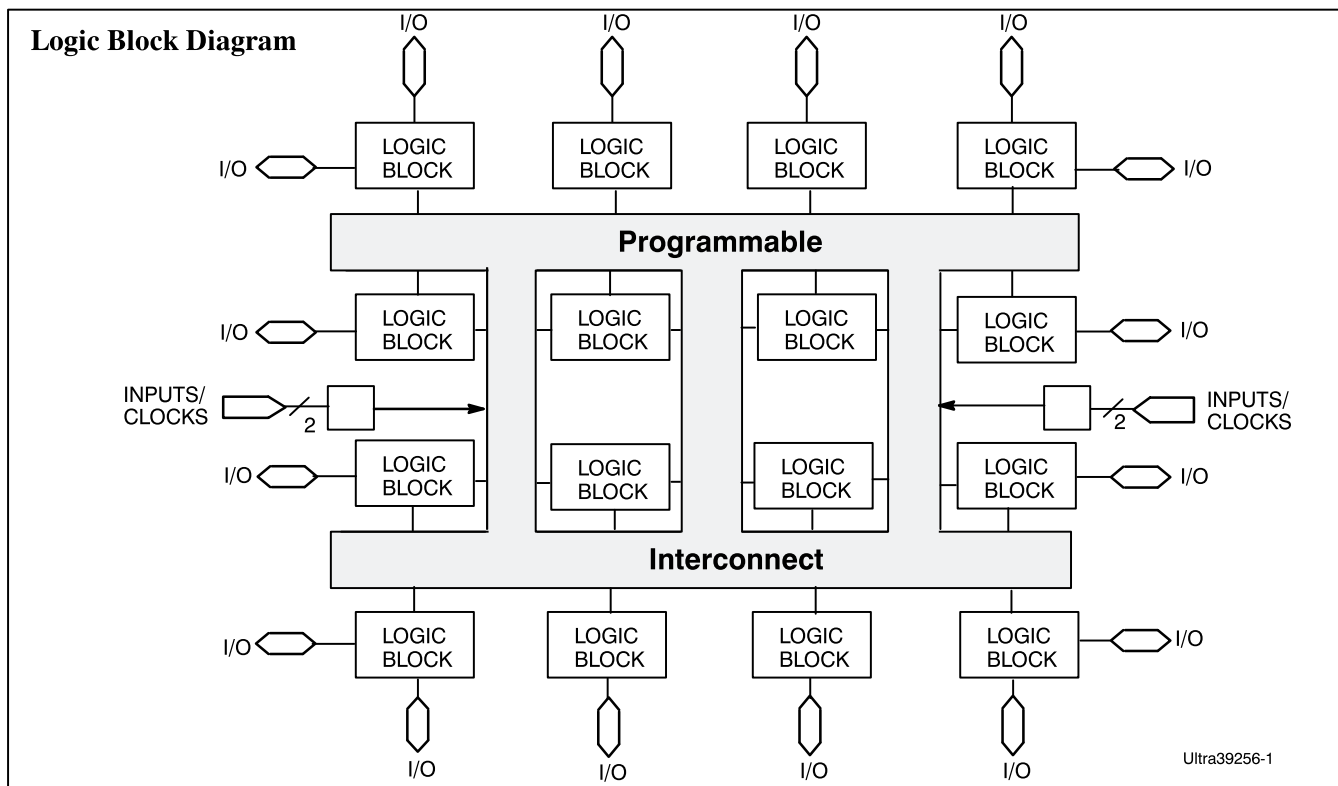
The Ultra39256 is a high-density, high-performance Complex Programmable Logic Device (CPLD) providing in-system reprogrammability (ISR) and full Joint Test Action Group (JTAG IEEE1149.1) compatibility. It is part of the Ultra39000™ Family of fast, reconfigurable CPLDs, which has been designed to bring high performance and the ease of use of 22V10s to ultra high-density PLDs. The entire family is also fully compliant with the PCI Local Bus Specification and will operate at either 3.3V or 5V.

The 256 macrocells in the Ultra39256 are divided between 16 logic blocks. Each logic block contains 16 macrocells along with a product term array and a fast, intelligent product term matrix. Each logic block in the Ultra39000 architecture is connected through a Programmable Interconnect

that produces extremely fast and predictable paths through the device.

All members of the Ultra39000 family feature an abundant number of I/O resources. The Ultra39256 contains either 128 or 160 I/O pins, as well as four dedicated inputs/clocks, and provides both fast synchronous and asynchronous clocking capabilities.

Additionally, the Ultra39256 features a programmable speed/power option that allows users to optimize designs for either ultra-fast performance or ultra-low power. The family also provides slew rate control for each of the outputs which reduces switching noise. And finally, the Ultra39256 features a very simple timing model that results in parameters that are not dependent on the device resources utilized or the type of application being implemented.



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