TITLE: Simulation of superconducting fault current limiter for analysis of voltage sag analysis .

ABSTRACT:

In this paper, a resistive type SFCL model was implemented by integrating Simulink and SimPowerSystem blocks in Matlab for analysis of voltage sag. Superconducting fault-current limiters (SFCLs) offer several benefits for electrical distribution systems. Due to the grid connection of the micro grids with the current power grids, excessive fault current is a serious problem to be solved for successful implementation of micro grids. Faults occurring in power distribution systems or facilities in plants generally cause the voltage sag or swell. The SFCL can not only limit the fault current to an acceptable value, but also mitigate the voltage sag.

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